## EECS 16A Designing Information Devices and Systems I

## 1. A Trans-Impedance Amplifier



Above is a picture of the equivalent circuit of an op-amp. Consider the following circuit with an ideal op amp in negative feedback:

(a) Use the golden rules to calculate $v_{\text {out }}$ as a function of $I_{s}$ and $R$.
(b) Use the golden rules to implement the same behavior as the above circuit (with a current source), but use instead a voltage source and resistor.

## 2. Op-Amp Golden Rules

On the left is the picture of the equivalent circuit of an op-amp for reference.

(a) What are the currents flowing into the positive and negative terminals of the op amp? I.e., what are $I^{+}$and $I^{-}$? What are some of the advantages of your answer with respect to using an op amp in your circuit designs?
(b) Suppose we add a resistor of value $R_{L}$ between $v_{\text {out }}$ and ground. What is the value of $v_{\text {out }}$ ? Does your answer depend on $R_{L}$ ? In other words, how does $R_{L}$ affect $A v_{\text {in }}$ ? What are the implications of this with respect to using op amps in circuit design?
(c) Now consider the circuit on the right. Assuming this is an ideal op amp, what is $v_{\text {out }}$ ?
(d) Assume for the op-amp that $R_{\text {in }} \rightarrow \infty$ and $R_{\text {out }} \rightarrow 0$. Draw the equivalent circuit for this op-amp and calculate $v_{\text {out }}$ in terms of $A, V$ and $R_{L}$. Does $v_{\text {out }}$ depend on $R_{L}$ ? What is $v_{\text {out }}$ in the limit as $A \rightarrow \infty$ ?

## 3. Op-amps as Comparators

For each of the circuits shown below, plot $V_{\text {out }}$ for $V_{\text {in }}$ ranging from -10 V to 10 V for (a) and from 0 V to 10 V for (b). Let $\mathrm{A}=100$ for your plots.
(a)



